2008 SWE Emerging Leader Award

Ramune Nagisetty

Intel Corporation

Ramune Nagisetty received her bachelor's degree in electrical engineering from Northwestern University in 1991 and her master's, specializing in solid state physics, from the University of California, Berkeley.

Nagisetty joined Intel Corporation in 1995, working more than 10 years in device physics and process technology for the company's logic technology development group. Her technical accomplishments have consistently demonstrated her ability to synthesize complex information, understand relationships between various process variables and electrical results, comprehend the results, and determine a path forward.

She has carried these skills into her current role as director of cross-Intel strategic technology programs. Nagisetty works with other senior technologists across Intel to understand opportunities and gaps in the company's future technology roadmaps. She is responsible for several technology programs that report directly to senior and executive management, including: the Integrated Silicon Technology Roadmap for 16 nm process and packaging technology, World Map Global Technology Opportunities, and Technology Strategic Long Range Plan.

During the development of Intel's 180 nm technology, Nagisetty identified specific transistor geometry issues as the root cause for the product performance shortfall. She recommended the implementation of two critical process changes that resolved the geometry issues and the performance shortfall.

Her leadership in the development of Intel's 65 nm process technology earned her the Intel Achievement Award. She also was the lead device engineer in the successful ramp of Intel's — and the world's — first wafer-size manufacturing process.

CITATION:

For demonstrated leadership in product research and design engineering.

Nagisetty has eight technical publications and four issued or pending patents related to device physics and high-performance process technology. She chaired Intel's mobility working group and drove development of new electron and hole mobility and stress characterization.

Serving as a role model and mentor for technical women is a priority for Nagisetty. Since participating in Intel's Women Principal Engineer's Forum in 2006, she is committed to building women's confidence and solidarity. She supports women's forums by speaking about her achievements and ongoing work to inspire more junior women. She spends several hours each week working with Intel women in groups and individually to discuss challenges they face, offering support and ideas. She also participates in Intel's university fellows program.

For Nagisetty, mentoring and being mentored are two-way processes. It's not about prescribing a solution to a problem. Rather, it's about having an ongoing conversation and creating an encouraging space for an individual to develop an idea, get in touch with feelings, and to discover her potential.

In addition to being a mentor, Nagisetty is an active volunteer in her community. For many years, she organized the Adopt-A-Family food and gift drives for Intel's Logic Technology Development Group and has been a hospice volunteer.